

```
//Note: For the best effect, we suggest you view the program with the Source
Insight software.
```

[illegible]

```
//  
//XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX  
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
```

```

/*****
*****
*Header File
*****
*****/

#include "msp430G2955.h"
#include "image.h"

```

```

/*****
*****
* IO Port Define
*****
*****/

```

```
#define SDA_H      (P1OUT |=BIT7)           // P1.7
#define SDA_L      (P1OUT &=~BIT7)
#define SCLK_H     (P1OUT |=BIT6)           // P1.6
#define SCLK_L     (P1OUT &=~BIT6)
#define nCS_H      (P1OUT |=BIT5)           // P1.5
#define nCS_L      (P1OUT &=~BIT5)
#define nDC_H      (P1OUT |=BIT4)           // P1.4
#define nDC_L      (P1OUT &=~BIT4)
#define nRST_H     (P1OUT |=BIT3)           // P1.3
#define nRST_L     (P1OUT &=~BIT3)
#define R_SDA      0x80    //P1.7
```

```
#define WRITE_OTP_OK 0
#define WRITE_OTP_FAIL_NO_VCOM 1
#define WRITE_OTP_FAIL_HAS_Written 2
#define WRITE_OTP_FAIL_WRONG_OPERATION 3
#define WRITE_OTP_FAIL_CRC_FAIL 4
#define WRITE_OTP_OK_WITH_DATA 5
```

```
unsigned char tempvalue;
```

```

unsigned char tempvalue1;
unsigned char temp1;
unsigned char temp2;
unsigned char Byte1,Byte2,Byte3,Byte4;
unsigned char vcomExist=0;
unsigned char vcomBuf=0;
unsigned char opt_write_status=WRITE_OTP_FAIL_NO_VCOM;
unsigned int opt_data_idx=0;
unsigned char tempBuff [112];
unsigned char paraBuff [50];

unsigned int temp,CRC_temp;
unsigned int Check_MTPdata,Check_Userdata;
unsigned char CRC_Byte;
unsigned char CRC_bite1,CRC_bite2,CRC_bite3,CRC_bite4;
unsigned char UserCRC_bite1,UserCRC_bite2,UserCRC_bite3,UserCRC_bite4;

/*****
*****
* Image-Display define
*****
*****/
#define PIC_BLACK      252
#define PIC_WHITE      255
#define PIC_A          1
#define PIC_B          2
#define PIC_HLINE      3
#define PIC_VLINE      4
#define PIC_C          5
#define PIC_D          6
#define PIC_E          7
#define PIC_YELLOW     9
#define PIC_RED        8
#define PIC_BLUE       10
#define PIC_GREEN      11

/*****
*****
* MCU delay configuration
*****
*****/

void DELAY_100nS(int delaytime)                // 30us
{
    int i,j;

    for(i=0;i<delaytime;i++)
        for(j=0;j<8;j++);
}

```

```

void DELAY_mS(int delaytime)                                // 1ms
{
    int i,j;

    for(i=0;i<delaytime;i++)
        for(j=0;j<1600;j++);
}

void DELAY_S(int delaytime)                                  // 1s
{
    int i,j,k;

    for(i=0;i<delaytime;i++)
        for(j=0;j<1000;j++)
            for(k=0;k<1600;k++);
}

/*****
*****
* IC Read Busy
*****
*****/

void READBUSY()
{
    while(1)
    {
        _NOP();
        if((P1IN & 0x04)==0x04)
            break;
    }
}

/*****
*****
* IC reset
*****
*****/

void RESET()
{
    nRST_L;
    DELAY_mS(20);
    nRST_H;
    DELAY_mS(10);
    nRST_L;
    DELAY_mS(20);
    nRST_H;
    DELAY_mS(10);
    READBUSY();
    DELAY_mS(10);
}

```

```

}
/*****
*****
* 4line SPI Write Command
*****
*****/

void SPI4W_WRITECOM(unsigned char INIT_COM)
{
    unsigned char TEMPCOM;
    unsigned char scnt;
    P1DIR |= R_SDA;
    TEMPCOM=INIT_COM;
    nCS_H;
    nCS_L;
    SCLK_L;
    nDC_L;
    for(scnt=0;scnt<8;scnt++)
    {
        if(TEMPCOM&0x80)
            SDA_H;
        else
            SDA_L;
        SCLK_H;
        SCLK_L;
        TEMPCOM=TEMPCOM<<1;
    }
    nCS_H;
}
/*****
*****
* 4line SPI Write Data
*****
*****/

void SPI4W_WRITEDATA(unsigned char INIT_DATA)
{
    unsigned char TEMPCOM;
    unsigned char scnt;
    P1DIR |= R_SDA;
    TEMPCOM=INIT_DATA;
    nCS_H;
    nCS_L;
    SCLK_L;
    nDC_H;
    for(scnt=0;scnt<8;scnt++)
    {
        if(TEMPCOM&0x80)
            SDA_H;
        else
            SDA_L;
        SCLK_H;
        SCLK_L;
        TEMPCOM=TEMPCOM<<1;
    }
}

```

```

        nCS_H;
    }

    /*****
    *****/
    * 4line SPI Read Data
    *****/

    unsigned char SPI4W_READDATA(void)
    {
        unsigned char scnt,temp;
        P1DIR &=~ R_SDA;
        nCS_H;
        nCS_L;
        SCLK_H;
        nDC_H;
        for(scnt=0;scnt<8;scnt++)
        {
            SCLK_H;
            if(P1IN&R_SDA)
                temp=(temp<<1)|0x01;
            else
                temp=temp<<1;
            SCLK_L;
        }
        nCS_H;
        tempvalue= temp;
        return temp;
    }

    /*****
    *****/
    * Temperature sensing
    *****/

    void read_tempereture(void)
    {

        SPI4W_WRITECOM(0x40);
        DELAY_S(1);
        temp1=SPI4W_READDATA();
        temp2=SPI4W_READDATA();

        tempvalue=temp1;
        tempvalue=temp2;

    }

```

```

/*****
*****
* Read Revision function
*****
*****/

void read_Revision()
{
    SPI4W_WRITECOM(0x70);
    Byte1=SPI4W_READDATA();//chip id
    Byte2=SPI4W_READDATA();//lut_ver[0:7]
    Byte3=SPI4W_READDATA();//lut_ver[8:15]
    Byte4=SPI4W_READDATA();//lut_ver[23:16]
}

/*****
*****
* EPD init
*****
*****/

void INIT_JD7601()
{

    SPI4W_WRITECOM(0xE9);
    SPI4W_WRITEDATA(0x01);

}

/*****
*****
* Enter DeepSleep
*****
*****/

void enterdeepsleep()
{
    SPI4W_WRITECOM(0x07); // Deep sleep
    SPI4W_WRITEDATA(0xA5);
}

/*****
*****
* Display Image
*****
*****/

void dis_img(unsigned char num)
{

```

```

unsigned int row, col;
unsigned int pcnt;

SPI4W_WRITECOM(0x10); // DTM1 Write
READBUSY();
pcnt = 0;
for(col=0; col<720; col++)
{
    for(row=0; row<240; row++)
    {
        switch (num)
        {
            case PIC_A:
//          SPI4W_WRITEDATA(gImage_Font[pcnt]);
            break;

            case PIC_GREEN:
//Display Green
                SPI4W_WRITEDATA(0x66);
            break;

            case PIC_BLUE:
//Display Blue
                SPI4W_WRITEDATA(0x55);
            break;

            case PIC_YELLOW:
//Display Yellow
                SPI4W_WRITEDATA(0x22);
            break;

            case PIC_RED:
//Display Red
                SPI4W_WRITEDATA(0x33);
            break;

            case PIC_BLACK:
//Display Black
                SPI4W_WRITEDATA(0x00);
            break;

            case PIC_WHITE:
//Display White
                SPI4W_WRITEDATA(0x11);
            break;

            default:
            break;
        }
        pcnt++;
    }
}

SPI4W_WRITECOM(0x04); // Power ON
READBUSY();

```


